

Fig. 1

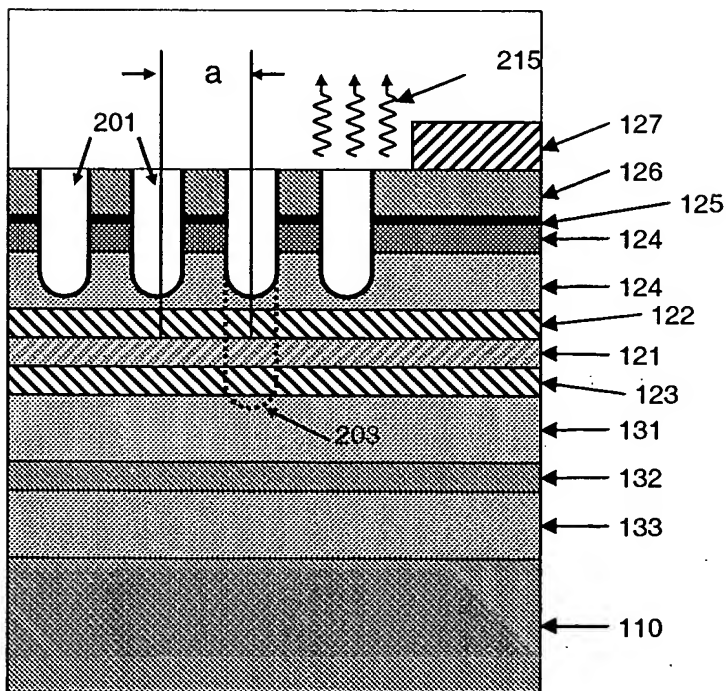


Fig. 2

FIG. 3A

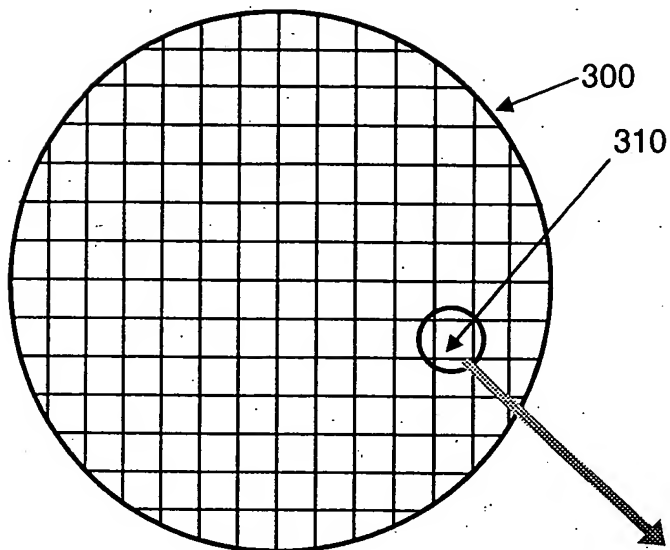
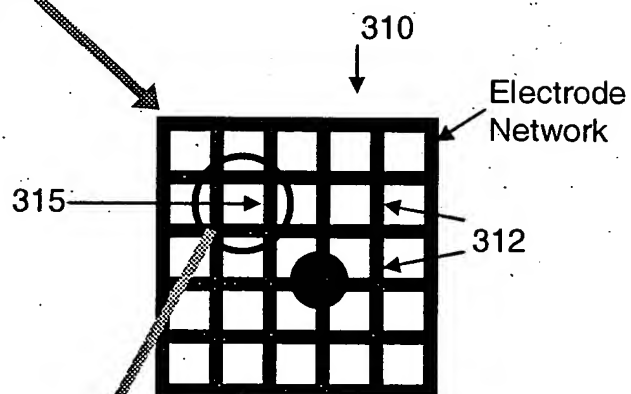


FIG. 3B



Blow-up view of a chip

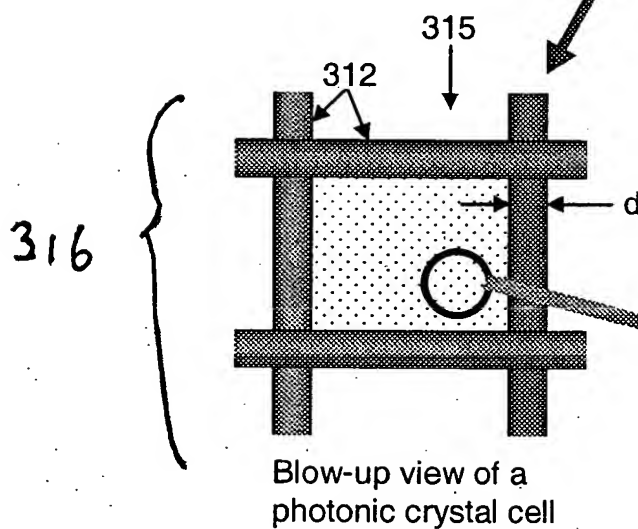
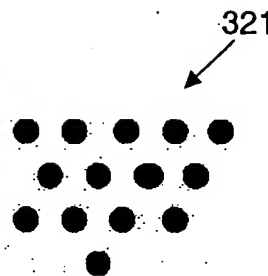
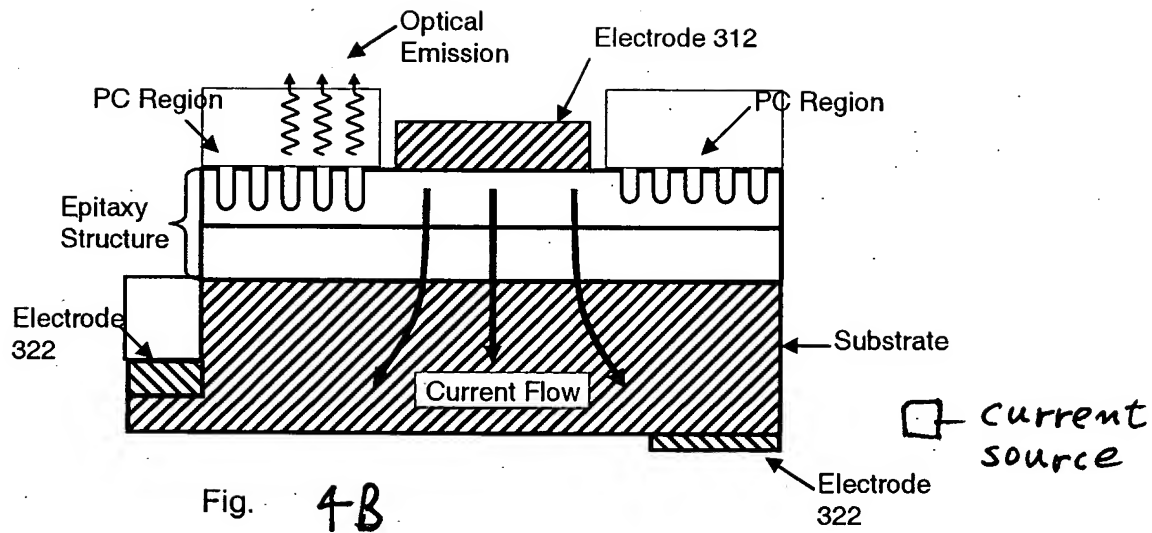
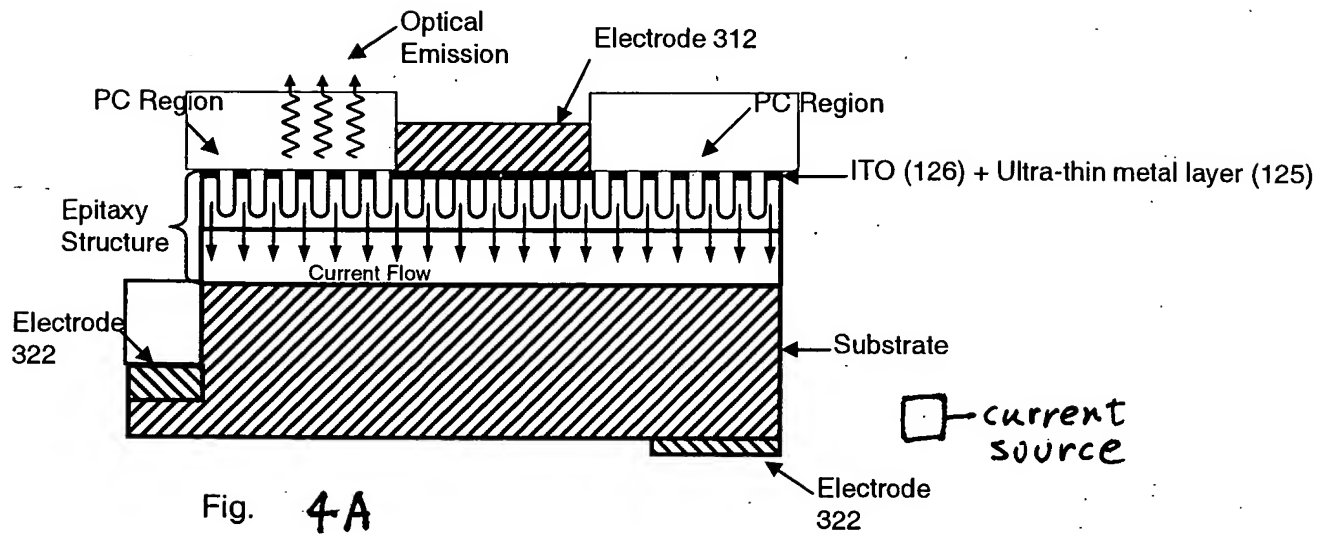
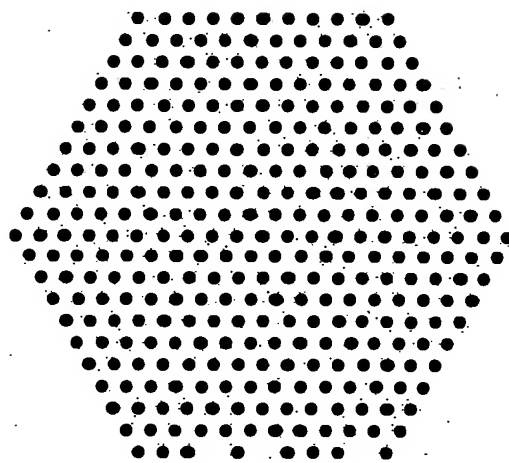
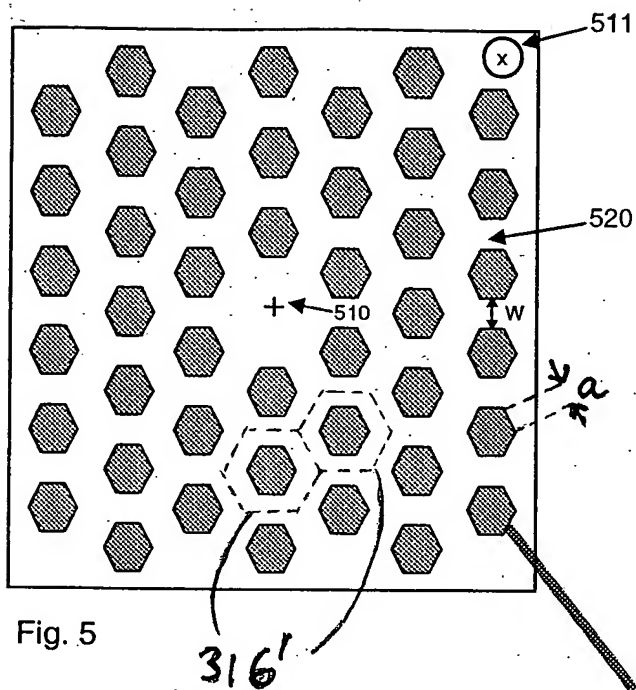


Fig. 3 C

FIG. 3D







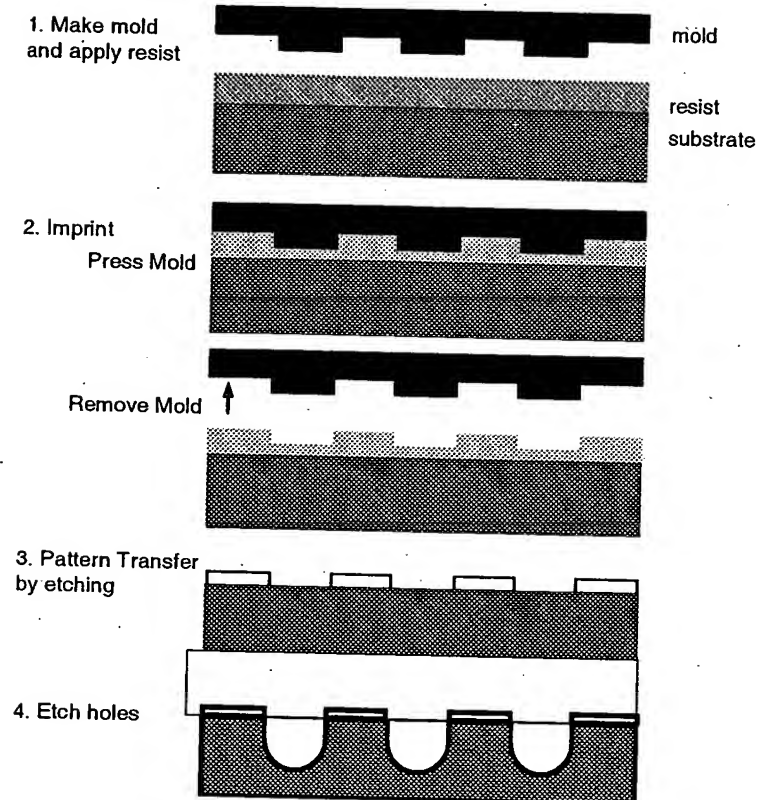


Fig. 7